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| **KLS Gogte Institute of Technology**  **Department of Computer Science and Engineering**  **IA- II**  **Subject : Logic Design and Applications Code : 15CS32 Semester : III Division : A,B,C Date:06/10/2016 Time: 1pm-2pm Max. Marks: 25** |
| **Note: i) Answer all questions.**  **ii) All questions carry *five* marks**   1. Explain the working of clocked JK flip flop **[L2,CLO 3,PO 1,2]** 2. Illustrate how T flip flop can be used as toggle switch with neat output waveform.   [**L3, CLO 3,PO 1,2,3]**   1. Discuss the steps to convert SR flip flop to JK flip flop with neat diagram   **[L2, CLO 3, PO 1,2]**   1. List out different types of shift registers and explain the working of PIPO shift register.   **[L1,L2, CLO 3,PO 1,2]**   1. Write the characteristic equation for SR and JK flip flop. **[L3,CLO 3,PO 1,2,3]** |